AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Serial Number: 09/470,329

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Title: PREVENTION OF LIVE-LOCK IN A MULTI-PROCESSOR SYSTEM

IN THE SPECIFICATION

Please amend the specification through the following paragraph substitutions. The specific changes are shown in the following marked-up versions of the original paragraphs.

The disclosure was objected to because of the following: on page 5, line 9, -5,879,656-, should be 5,897,656.

Please replace the paragraph beginning at page 5, line 5 with the following paragraph:

The multiple-bus system also includes a novel system access controller 130 which is capable of preventing such live-lock problems. Generally speaking, the system access controller 130 controls the operation of the multiple-bus system. An example embodiment of a system access controller for controlling the operation of a multiple-bus system is described in US Pat. No. 5,879,656 5,897,656 entitled "System and Method for Maintaining Memory Coherency in a Computer System Having Multiple System Buses." The novel system access controller 130, according to one embodiment of the present invention, includes a live-lock prevention mechanism. Such a system access controller 130 is described in more detail by reference to FIG. 2.